

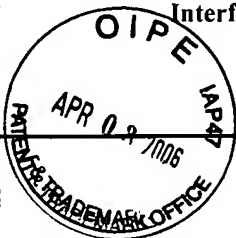
**COMBINED NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE
BOARD OF PATENT APPEALS AND INTERFERENCES & PETITION FOR
EXTENSION OF TIME UNDER 37 C.F.R. 1.136(a) (Large Entity)**

Docket No.
872.0100.U1(US)

In Re Application Of: **A. Ruha et al.**

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/005,766	11/02/2001	Tran, Pablo N.	29683	2685	7390

Invention: **Multi-Mode I/O Circuitry Supporting Low Interference Signaling Schemes For High Speed Digital Interfaces**



TO THE COMMISSIONER FOR PATENTS:

This combined Notice of Appeal from the Primary Examiner to the Board of Patent Appeals and Interferences and petition for extension of time under 37 CFR 1.136(a) is respectfully submitted by the undersigned:

Dated: March 30, 2006

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